

ZXGD3004E6 8A(peak) gate driver in SOT23-6

General description

The ZXGD3004E6 is a high-speed non-inverting single MOSFET gate driver capable of driving up to 8A into a MOSFET or IGBT gate capacitive load from supply voltages up to 40V. With typical propagation delay times down to ~1ns and rise/fall times down to 14ns this device ensures rapid switching of the power MOSFET or IGBT to minimize power losses and distortion in high current fast switching applications.

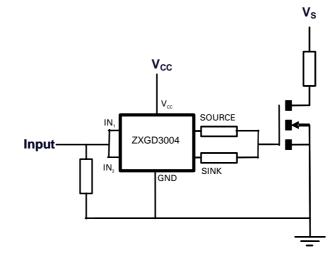
The ZXGD3004E6 is inherently rugged to latch-up and shoot-through, and its wide supply voltage range allows full enhancement to minimize on-losses of the power MOSFET or IGBT.

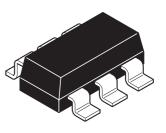
Its low input voltage requirement and high current gain allows high current driving from low voltage controller ICs, and the optimized pin-out SOT23-6 package with separate source and sink pins eases board layout, enabling reduced parasitic inductance and independent control of rise and fall slew rates.

Features

- 40V operating voltage range
- 8 amps peak output current
- Fast switching emitter-follower configuration
 - 1ns propagation delay time
 - 14ns rise/fall time, 1500pF load
- Low input current requirement
 - 1.9A(source)/1.9A(sink) output current from 10mA input
- SOT23-6 package
- · Separate source and sink outputs for independent control of rise and fall time
- · Optimized pin-out to ease board layout and minimize trace inductance
- No Latch Up
- No shoot through
- · Near Zero quiescent and output leakage current

Typical application circuit



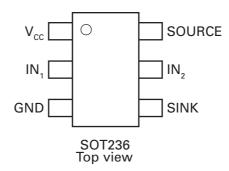


Applications

Power MOSFET and IGBT Gate Driving in

- Synchronous switch-mode power supplies
- Secondary side synchronous rectification
- Plasma Display Panel power modules
- 1, 2 and 3-phase motor control circuits
- Audio switching amplifier power output stages

Pin configuration



Pin description

Pin Name	Pin Function
V _{CC}	Driver supply.
IN_1 / IN_2	Driver input pins. These are normally connected together by circuit tracks.
GND	Ground.
SOURCE	Source current output.
SINK	Sink current output.

Ordering information

Device	Reel size	Tape width	Quantity per
	(inches)	(mm)	reel
ZXGD3004E6TA	7	8 embossed	3000

Device marking

3004

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Supply voltage	V _{CC}	40	V
Input voltage	V _{IN}	40	V
Peak sink current ^(c)	I _(sink) PK	8	А
Source current @ I _{IN1} + I _{IN2} =10mA ^(a)	I _(source)	1.9	А
Sink current @ I _{IN1} + I _{IN2} =10mA ^(a)	I _(sink)	1.9	А
Input current ^(c)	I _{IN1} , I _{IN2}	1	А
Power dissipation at $T_A = 25^{\circ}C^{(a)(b)}$	PD	1.1	W
Linear derating factor		8.8	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150	°C

Thermal resistance

Parameter	Symbol	Value	Unit
Junction to ambient ^{(a)(b)}	R_{\ThetaJC}	113	°C/W

NOTES:

(a) For a device surface mounted on 25mm x 25mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

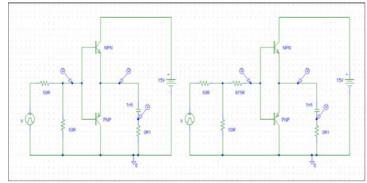
(b) For device with two active dice running at equal power.

(c) Pulse width <= 300 us limit repetition rate to comply with maximum junction temperature.

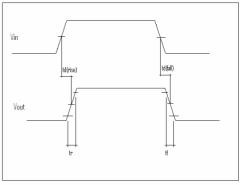
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Output voltage, high	V _{OH}		V _{IN1} – 0.4		V	I _{source} = 1μA
Output voltage, low	V _{OL}		V _{IN1} + 0.4		V	$I_{sink} = 1\mu A$
Source output leakage current	I _{L(source)}			1	μΑ	$V_{CC} = 40V,$ $V_{IN1} = V_{IN2} = 0V$
Sink output leakage current	I _{L(sink)}			1	μA	$V_{CC} = 40V,$ $V_{IN1} = V_{IN2} = V_{CC}$
Quiescent current	۱ _۵			50	nA	$V_{CC} = 32V,$ $V_{IN1} = V_{IN2} = 0V$
Source output current	I _(source)	1.2	1.9		А	$I_{\rm IN1}+I_{\rm IN2}=10\rm{mA}$
Sink output current	I _(sink)	1.2	1.9		А	$I_{\rm IN1}+I_{\rm IN2}=10\rm{mA}$
Source output current	I _{(source)PK}		8		А	$I_{\rm IN1}+I_{\rm IN2}=1A$
Sink output current	I _{(sink)PK}		8		А	$I_{\rm IN1}+I_{\rm IN2}=1A$
Gate driver switching times	t _{d(rise)} t _r t _{d(fall)} t _f		1.1 13.4 0.95 12.4		ns ns ns ns	$\begin{array}{l} C_{L} = 1.5 nF, \ R_{L} = 0.1 \Omega, \\ V_{CC} = 15 V, \ V_{IN} = 12.5 V, \\ R_{S} = 25 \Omega \end{array}$
Gate driver switching times	t _{d(rise)} t _r t _{d(fall)} t _f		3.2 77.9 3.6 82		ns ns ns ns	$\begin{array}{l} C_{L}{=}1.5nF, R_{L}{=}0.1\Omega,\\ V_{CC}{=}15V, V_{IN}{=}12.5V,\\ R_{S}{=}1k\Omega \end{array}$

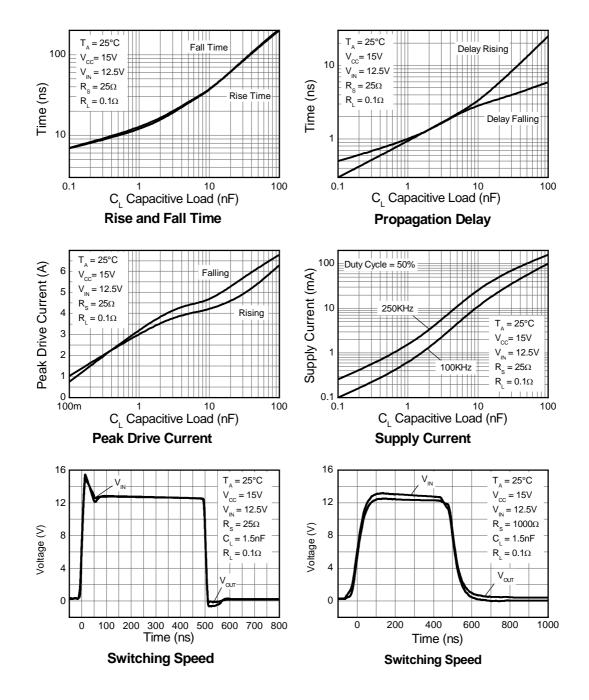
Electrical characteristics (at Tamb = 25°C unless otherwise stated).

Switching time test circuits

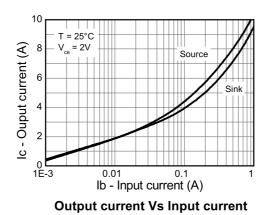


Timing diagram

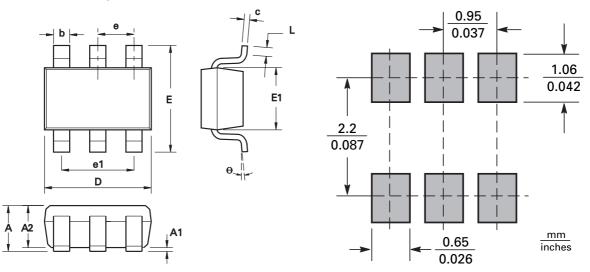




Typical gate driver characteristics



SOT23-6 Package outline



DIM	Millimeters		Inc	hes	
	Min.	Max.	Min.	Max.	
A	0.90	1.45	0.0354	0.0570	
A1	0.00	0.15	0.00	0.0059	
A2	0.90	1.30	0.0354	0.0511	
b	0.35	0.50	0.0078	0.0196	
С	0.09	0.26	0.0035	0.0102	
D	2.70	3.10	0.1062	0.1220	
E	2.20	3.20	0.0866	0.1181	
E1	1.30	1.80	0.0511	0.0708	
L	0.10	0.60	0.0039	0.0236	
е	0.95	REF	0.037	4 REF	
e1	1.90	REF	0.074	8 REF	
L	0°	30°	0°	30°	

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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Zetex sales offices

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH Kustermann-park Balanstraße 59 D-81541 München Germany	Zetex Inc 700 Veterans Memorial Highway Hauppauge, NY 11788 USA	Zetex (Asia Ltd) 3701-04 Metroplaza Tower 1 Hing Fong Road, Kwai Fong Hong Kong	Zetex Semiconductors plc Zetex Technology Park, Chadderton Oldham, OL9 9LL United Kingdom
Telefon: (49) 89 45 49 49 0 Fax: (49) 89 45 49 49 49 europe.sales@zetex.com	Telephone: (1) 631 360 2222 Fax: (1) 631 360 8222 usa.sales@zetex.com	Telephone: (852) 26100 611 Fax: (852) 24250 494 asia.sales@zetex.com	Telephone: (44) 161 622 4444 Fax: (44) 161 622 4446 hq@zetex.com

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